

Call for Participation

ASP-DAC 2014

19th Asia and South Pacific Design Automation Conference

Date: January 20-23, 2014

Place: Suntec, Singapore

Features of ASP-DAC 2014

■ Keynote Speeches

- **Ivo Bolsens** (Senior VP & CTO, Xilinx, U.S.A.) "*All Programmable SOC FPGA for Networking and Computing in Big Data Infrastructure*"
- **Georges Gielen** (Katholieke Univ. Leuven, Belgium) "*Designing Analog Functions without Analog Transistors*"
- **Kaushik Roy** (Purdue Univ., U.S.A.) "*Beyond Charge-Based Computing*"
- **Ulf Schneider** (Managing Director, Lantiq Asia Pacific Pte Ltd/President, SSIA, Singapore) Banquet Keynote

■ Tutorials

Participants can choose two 3-hour tutorials – one in the morning session and one in the afternoon. For each session, four options are available – two in the physical-design domain and two in the system-design domain.

■ University LSI Design Contest

10 papers were selected from 19 submissions.

■ Technical Sessions

9 special sessions and 26 normal sessions will be organized. For the normal sessions, 108 papers were selected from 343 submissions.

Sponsors:

ACM/SIGDA, IEEE CASS, IEEE CEDA,
Singapore Chapter of CASS

Conference Secretariat

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ASIA SOUTH PACIFIC
DAC DESIGN
AUTOMATION
CONFERENCE

Aims of the Conference

ASP-DAC 2014 is nineteenth annual international conference on VLSI design automation in Asia and South Pacific regions, one of the most active regions of design and fabrication of silicon chips in the world. The conference aims at providing the Asian and South Pacific CAD/DA and Design community with opportunities of presenting recent advances and with forums for future directions in technologies related to Electronic Design Automation (EDA).

For more detail information,
the Advanced Programme will be announced on
ASP-DAC website around mid October, 2013.

<http://www.aspdac.com>